



**California State Polytechnic University, Pomona
DEGREE REQUIREMENT EVALUATION**

ELM Satisfied ___ Yes ___ No
 EPT Satisfied ___ Yes ___ No
 GWT Satisfied ___ Yes ___ No

MAJOR (PLAN) **COMPUTER ENGINEERING**

OPTION/EMPHASIS (SUB-PLAN) _____

UNITS REQUIRED **198**

NAME _____

LAST FIRST MI

STUDENT I.D. # _____

TERM ADMITTED _____ YEAR: **2005-2006**

EVALUATOR _____

DATE _____

UPDATES _____

CORE COURSES	Units	SUPPORT COURSES	Units	IGE	GENERAL EDUCATION. Students may fulfill these requirements at Cal Poly Pomona with the General Education (GE) or the Interdisciplinary General Education (IGE) Programs. Select courses from approved lists shown in the Schedule of Classes unless specified.
<i>Students in this major are expected to maintain a GPA of at least 2.00 in all core courses.</i>		Gen Chemistry	CHM 121/121L	4	IGE 120 4
Intro to EE	ECE 109/109L 4	Anal Geom/Calculus II	MAT 115	4	IGE 121 4
C for Engineers	ECE 114/114L 4	Anal Geom/Calculus III	MAT 116	4	IGE 122 4
Discrete Structures	ECE 130 4	Calculus Sev Var I	MAT 214	3	IGE 220 4
Intro to Combinational Logic	ECE 204/204L 4	Calculus Sev Var II	MAT 215	3	IGE 221 4
Intro to Sequential Logic	ECE 205/205L 4	Diff Equations	MAT 216	4	IGE 222 4
Network Analysis I	ECE 207/207L 4	Gen Physics	PHY 132	3	IGE 223 4
Network Analysis II	ECE 209/209L 4	Gen Physics	PHY 133/L	4	IGE 224 4
Electronic Devices and Circuits	ECE 220/220L 5				COM 204 4
Object-Oriented Programming	ECE 256 4				<u>ECE 311</u> 4
Electromagnetic Fields	ECE 302 4				EC 201/202 4
Data Structures for Engineers	ECE 304 4				Area B 16
Intro Disc Time Sig and Sys	ECE 306 4				Area C4 4
Comp Simulation of Dyn Sys	ECE 306L 1				Area D4 4
Control Systems Engineering	ECE 309/309L 5				
Prob, Stats, & Random Processes for ECE	ECE 315 4				
Electronic Design for Digital Circuits	ECE 325/325L 4				
Intro to Microcontrollers	ECE 341/341L 4				
Computer Organization	ECE 342/342L 5				
<u>OR</u> Microprocessor I	ECE 343/343L (5)				
Digital Design using Verilog HDL	ECE 415/415L 4				
<u>OR</u> State Machine Design using VHDL	ECE 424/424L (4)				
Computer Architecture	ECE 425/425L 4				
Operating Systems	ECE 426/426L 4				
Applications Development using Java	ECE 429 4				
Computer Networks	ECE 431/431L 5				
<u>OR</u> TCP/IP Internetworking	ECE 433/433L (4)				
Professional Topics for Engineers and Senior Design Team Project	ECE 464, 467 1,1				
Software Engineering	ECE 480 4				
ECE U.D. Elec*	2-3				
*May vary depending upon the selection of ECE 431/433.					
		SUMMARY OF ADVANCED STANDING CREDIT: Earned Hours _____ G.P.A. Hours _____ Quality Points _____ G.P.A. _____			
UNITS REQUIRED: 101		UNITS REQUIRED: 29		UNITS REQUIRED: 68	

GENERAL EDUCATION COURSES

Area A Communication and Critical Thinking–12 units

- 1 ENG 104 4
- 2 COM 204 4
- 3 ECE 311 or Elective 4

Area B Math and Natural Sciences–16 units

- 1 MAT 114 4
- 2 PHY 131/131L, 132L 5
- 3 Biological Science 3
- 4 EGR 481, 482 4

Area C Humanities– 16 units

- 1 Fine/Performing Arts 4
- 2 Philosophy and Civilization 4
- 3 Literature and Foreign Language 4
- 4 EGR 402 4

Area D Social Sciences– 20 units

- 1a PLS 201 4
- 1b HST 202 4
- 2 EC 201 or 202 4
- 3 Sociology, Anthropology, Ethnic & Gender Studies 4
- 4 Social Science Synthesis 4

Area E Lifelong Understanding and Self-Development–4 units

Underlined courses satisfy both major and general education requirements.

Computer Engineering Curriculum Flow Chart

Year 2005/2006

Name: _____

Freshman		
Fall (15)	Winter (16)	Spring (16)

Sophomore		
Fall(20)	Winter (18)	Spring(15)

Junior		
Fall (18)	Winter (17)	Spring (18)

Senior		
Fall (15/16)	Winter (14)	Spring(15)

