

Laboratory 1

CS 210
Fall 2004
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The following are taken from lab exercise 11-2, chapter 11 of Mano, "Digital Design."

- 1 Construct a circuit which implements an NOT gate (inverter) using only NAND gates.
- 2 Construct a circuit which implements a 2-input AND gate using only NAND gates.
- 3 Construct a circuit which implements a 2-input OR gate using only NAND gates.
- 4 Construct a circuit which implements a 2-input NOR gate using only NAND gates.
- 5 Construct a circuit which implements a 2-input XOR gate using only NAND gates.
- 6 Construct a circuit which implements the expression $AB+CD$ using only NAND gates.